

REMARKS/ARGUMENTS

Favorable reconsideration of this application as presently amended and in light of the following remarks is respectfully requested

Claims 1, 4-6, 21-24 and 39-41 are presently active in this application; Claims 1, 6, 14, 21, 25, 26 and 32 having been amended by the present amendment, and Claims 2, 3, 7-20 and 25-38 having been withdrawn from consideration as directed to a non-elected invention.

In the outstanding Office Action the title was objected to as not being descriptive; Figure 8 was objected to as including an informality requiring correction; Figs 20-26 were objected to as requiring the designated legend of --PRIOR ART--; Claims 1, 4-6, 21-24 and 39-41 were objected to as including informalities requiring correction; Claims 1, 4-6, 21-24 and 39-41 were rejected under 35 USC §112, second paragraph, as being indefinite; Claims 1, 4-6, 21-24 and 39-41 were further rejected under 35 USC §103(a) as being unpatentable over Narazaki et al (6,285,058, hereinafter called "Narazaki") in view of Applicants' admitted prior art.

In response to the objection to the Title, the title has been amended to better reflect the claimed invention. Accordingly, the objection to the title is believed to have been overcome.

In response to the objection to the drawings, the specification has been amended to be consistent with the depiction of the substrate 801 in Figure 9, and thus the objection to Figure 8 is believed to have been overcome. Further, Figures 20-26 have been amended to include the label of --Prior Art--. Accordingly, the objection to Figures 20-26 has been overcome.

In response to the several grounds for objection to the claims and the rejection under 35 USC §112, first and second paragraphs, the claims have been amended consistent with Applicants' disclosure as shown in Fig. 1, wherein an upper surface of the second source electrode 114 is higher than an upper surface of the second gate wiring 110 and consistent

Application No. 10/612,925

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IN THE DRAWINGS

The attached sheet of drawings includes changes to Figs. 20-26. These sheets, which include Figs. 20-26, replace the original sheets including Figs. 20-26.

Attachment: Replacement Sheets

with page 11, lines 33-35 and page 13, lines 26-30 of the specification. Therefore, in view of the clarifying changes, it is respectfully submitted that the claims are definite and supported by the original specification and drawings. Accordingly, these grounds for objection and rejection are likewise believed to have been overcome.

Turning now to the outstanding grounds for rejection on the merits, it is pointed out that according to one aspect of the present invention stated in Claim 1, the upper surfaces of the main electrodes are higher than an upper surface of the gate wiring. As a result, it is possible to reduce shock applied to a first insulating film when a connecting plate is connected onto the upper surfaces of the main electrode.

On the other hand, according to Narazaki, an upper surface of a main electrode 14 is at the same level as an upper surface of a gate wiring 13. Therefore, shock is applied to a first insulating film 6 when a connecting plate is connected onto the upper surface of main electrode 14. Moreover, if the connecting plate is connected onto the upper surface of the main electrode 14, the gate wiring 13 is short-circuited to the main electrode 14. In view of this distinction, it is respectfully submitted that Claim 1, as well as Claims 6 and 25 which recite a similar feature, patentably distinguish over Narazaki.

Consequently, in view of the present amendment and in light of the above discussion, no further issues are believed to be outstanding, and the present application is believed to be in condition for formal allowance. An early and favorable action to that effect is respectfully requested.

Respectfully submitted,

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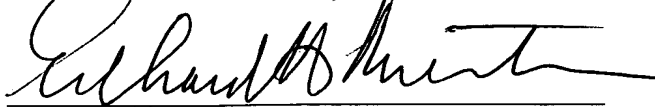
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